

55K

SWITCH	DEFINITION
1	Bit 4 (MSB) of hardware revision number
2	Bit 3 of hardware revision number
3	Bit 2 of hardware revision number
4	Bit 1 of hardware revision number
5	Bit 0 of hardware revision number
6	on for loop on diagnostics
7	undefined
8	undefined

59L

SWITCH	DEFINITION
1	Bit 5 (MSB) of PIO address 2
2	Bit 4 (MSB) of PIO address 2
3	Bit 3 (MSB) of PIO address 2
4	Bit 2 (MSB) of PIO address 2
5	Bit 1 (MSB) of PIO address 2
6	Bit 0 (LSB) of PIO address 2
7	on for status info to SCC port A, off for info to SCC port B
8	on for 16MHz 80286 clock speed, off for 8MHz clock speed

59K

SWITCH	DEFINITION
1	on for HP380 Disk drive on SCSI bus 1
2	on for HP380 Disk drive on SCSI bus 2
3	on for Cartridge tape drive on SCSI bus 1
4	on for Cartridge tape drive on SCSI bus 2
5	undefined
6	undefined
7	undefined
8	undefined

55L OR 59L

CONTROLLER NUMBER	DISK ADDRESS	SWITCHES TO TURN ON
1	'26	2, 4, 5
2	'27	2, 4, 5, 6
3	'22	2, 5
4	'23	2, 5, 6
5	'24	2, 4
6	'25	2, 4, 6
7	'45	1, 4, 6
8	'46	1, 4, 5

CONTROLLER NUMBER	TAPE ADDRESS	SWITCHES TO TURN ON
1	'14	3, 4
2	'13	3, 5, 6
3	'12	3, 5

55L

SWITCH	DEFINITION
1	Bit 5 (MSB) of PIO address 1
2	Bit 4 (MSB) of PIO address 1
3	Bit 3 (MSB) of PIO address 1
4	Bit 2 (MSB) of PIO address 1
5	Bit 1 (MSB) of PIO address 1
6	Bit 0 (LSB) of PIO address 1
7	On to force fill diagnostics, OFF for normal diagnostics
8	On for Debug mode, OFF for normal mode

Figure 2-16. SDTC Address/Cabling (Diff Quad Tape Drive)

Figure 2-16. SDTC Address/Cabling (Diff Quad Tape Drive)

